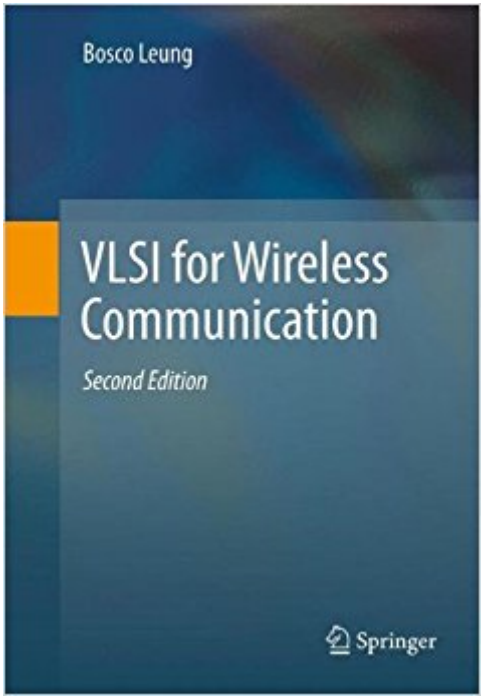
VLSI For Wireless Communication



Synopsis

VLSI for Wireless Communication, Second Edition, takes a system approach to its subject, starting with an overview of the most up to date wireless systems and the transceiver architecture available. Each chapter includes at least one complete design example.

Book Information

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Customer Reviews

VLSI for Wireless Communication 2nd edition, an advanced level text book, takes a system approach starting with an overview of the most up to date wireless systems and the transceiver architecture available today. Wireless standards are first introduced (updated to include the most recent 3G/4G standards in the second edition), and translates from a wireless standard to the implementation of a transceiver. This system approach is particularly important as the level of integration in VLSI increases and coupling between system and component design becomes more intimate.VLSI for Wireless Communication, 2nd edition illustrates designs with full design examples. Each chapter includes at least one complete design example that helps explain the architecture/circuits presented in this text. This book has close to 10 homework problems at the end of each chapter. A complete solution manual is available on-line.About this textbook: Includes an overview of the most up to date wireless systems and the transceiver architecture available today, including Wireless Standards.Design examples and exercises are provided in each chapter to enhance the students understanding, including practicing engineers.A complete solution manual is

available on-lineThis book is designed as a primary text book for upper -undergraduate level students and graduate level students concentrating on electrical engineering and computer science. Engineer professionals and researchers working in wireless communications, circuit design and development will find this book valuable as well. --This text refers to the Hardcover edition.

Bosco H. Leung received the B. Sc. Degree from Rensselaer Polytechnic Institute, Troy, New York, in 1979, M. Sc. Degree from the California Institute of Technology, Pasadena, in 1980 and the Ph.D. Degree from the University of California, Berkeley in 1987, both in electrical engineering. From 1980 to 1983, he was a circuit designer with Northern Telecom, Canada.He joined the Department of Electrical and Computer Engineering, University of Waterloo, Ontario, Canada in 1988, where he is now a professor. His main research interest is in mixed analog-digital integrated circuits, in particular, wireless communication circuits. He has published over fifty technical papers and has been granted fourÂ U. S. patents in this area.Dr. Leung was the Associate Editor for IEEE Transactions on Circuits and Systems on various occasions.

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